

The diagram illustrates a timing sequence over a horizontal axis from 0 to 50. A dashed horizontal line is positioned in the lower half of the diagram.

- Blue Signal:** A solid blue bar at the top, labeled with a white '1' at the start (time 0).
- Orange Signal:** A step function that starts at a low level, rises to a high level at time 5, falls back to low at time 12, rises again at time 30, falls at time 38, rises at time 45, and falls at time 50.
- Green Signal:** A step function that starts at a low level, rises to a high level at time 10, falls back to low at time 17, rises again at time 35, falls at time 42, and rises at time 49.
- Red Blocks:** A sequence of ten red blocks, each labeled with a white number from 1 to 10. They are arranged horizontally, starting at time 0 and ending at time 50, with a small gap between each block.
- Purple Bar:** A solid purple bar at the bottom, labeled with a white '0' at the start (time 0).

Timestamp (125MHz FPGA clock ticks)