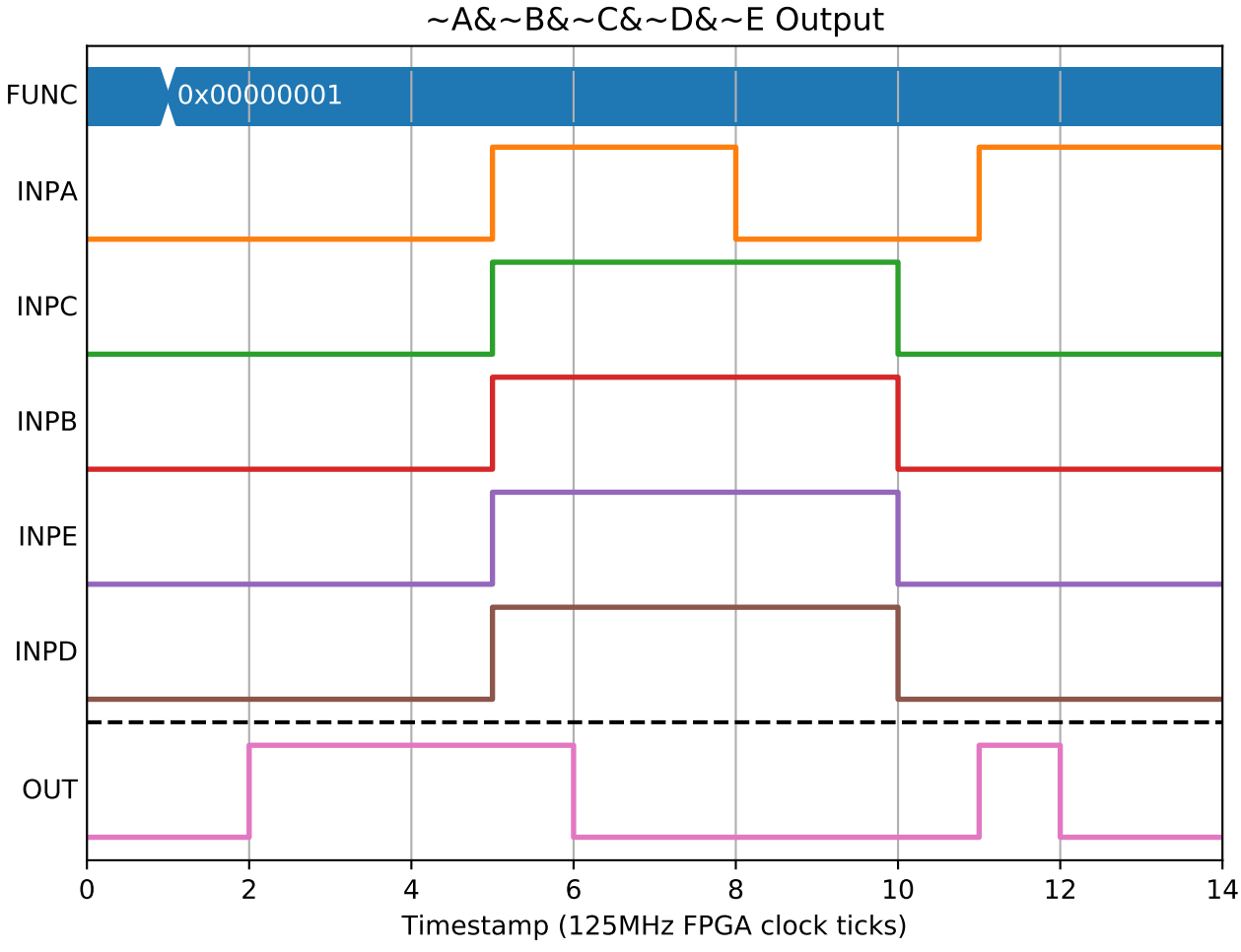
~A&~B&~C&~D&~E Output
FUNC
0x00000001
INPA
INPC
INPB
INPE
INPD
OUT
0
2
4
6
8
10
12
14
Timestamp (125MHz FPGA clock ticks)